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ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

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SCANNED 147-3 G.A. Am

PATENT DATE

APPLICATION NO. 09/837007	CONT/PRIOR	CLASS 257	SUBCLASS	ART UNIT <i>1821</i>	EXAMINER <i>Mitchell</i>
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APPLICANTS

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TITLE

Structure and manufacturing method of a chip scale package

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drawn	Figs. Drawn	Printed Fig.	Total Claims
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_____	_____ (Primary Examiner) (Date)		Amount Due	Date Paid
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